

Electronic Acknowledgement Receipt

EFS ID:	3452692
Application Number:	10570290
International Application Number:	
Confirmation Number:	5446
Title of Invention:	Intergrated circuit and a method of cache remapping
First Named Inventor/Applicant Name:	Adrianus Josephus Bink
Customer Number:	65913
Filer:	Mark Alan Wilson
Filer Authorized By:	
Attorney Docket Number:	NL031031
Receipt Date:	13-JUN-2008
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Application Type:	U.S. National Stage under 35 USC 371

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Payment Type	Deposit Account
Payment was successfully received in RAM	\$ 810
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Request for Continued Examination (RCE)	NL031031_RCETrans_031308.pdf	159424 aa1e52f2a61c5499e5e2e1ea678c5e56b33f4eba	no	1
Warnings:					
This is not a USPTO supplied RCE SB30 form.					
Information:					
2		NL031031_RCE_031308.pdf	94806 06eca65b0ce269411f6f46d885b4d5877771da9e7	yes	9
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment Submitted/Entered with Filing of CPA/RCE		1	1	
	Claims		2	4	
	Applicant Arguments/Remarks Made in an Amendment		5	9	
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8187 8e943cca0f0694c8f2b220f4d387de1f816577ee	no	2
Warnings:					
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Total Files Size (in bytes):			262417		
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